

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Grutzendiek et al. Confirmation No.: 4906  
Serial No.: 09/806,224 Examiner: George Fourson  
Filed: 06/11/2001 Group Art Unit: 2823  
Title: Method for Producing Transistors

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF ELECTRONIC TRANSMISSION

I hereby certify that this correspondence is being transmitted electronically to:  
Examiner George Fourson, Group Art Unit 2823, United States Patent and  
Trademark Office, Alexandria, VA 22313-1450, on June 29, 2007.

  
Jeff Rothberg  
Attorney for Applicant  
Reg. No. 26,429

Dated: June 29, 2007

RESPONSE TO NOTICE OF DRAWING INCONSISTENCY WITH SPECIFICATION

Dear Sir:

In the Specification:

Kindly replace existing pages 4, 6, 7, 8 and 9 with the accompanying similarly numbered replacement pages.